



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

JONATHAN P. DOUGLAS, ET AL.

Application No.: 10/615,918

Filed: July 8, 2003

For: **PIPELINED INSTRUCTION DECODER
FOR MULTI-THREADED PROCESSORS**

Art Group: 2111

Examiner: Kenneth S. Kim

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Some or all of the references listed on the enclosed PTO/SB/08 were previously identified in the parent application (Application No. 09/475,105, filed December 30, 1999) and copies of the references were furnished at that time. Accordingly, per 37 CFR §1.98(d)(1) additional copies of those references are not submitted herewith.

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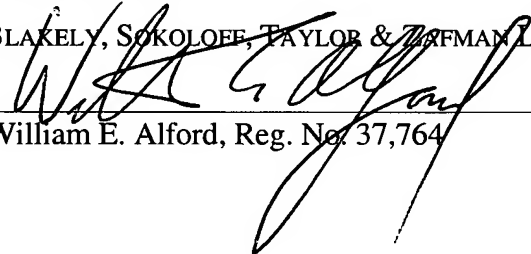
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZEFMAN LLP


Date: February 10, 2005



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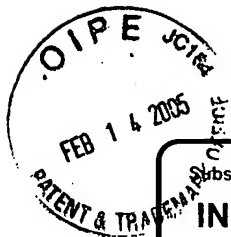
I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Tu T. Nguyen

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/615,618
				Filing Date	July 8, 2003
				First Named Inventor	Jonathan P. Douglas
				Art Unit	2183
				Examiner Name	Kim, Kenneth S.
Sheet	1	of	2	Attorney Docket Number	42P7098C

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code‡ (if known)			
		US-5,357,617	10-18-1994	Davis, et al.	
		US-5,778,246	07-07-1998	Brennan	
		US-5,890,008	06-25-1997	Panwar, et al.	
		US-5,913,049	06-15-1999	Shiell, et al.	
		US-5,983,339	11-09-1999	Klim	
		US-6,026,476	02-15-2000	Rosen	
		US-6,357,016	03-12-2002	Rodgers, et al.	
		US-6,385,719	05-07-2002	Derrick, et al.	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.†	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T‡
		Country Code§ - Number¶ - Kind Code§ (if known)				
		WO93/01545	07-07-1992	PCT		

Examiner Signature		Date Considered	
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†Applicant's unique citation designation number (optional). ‡See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. §Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ¶For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. §Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ¶Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
		EUGENE R. HNATEK; Random-Access Memories and Content-Addressable Memories, A User's Handbook of Semiconductor Memories; 19971 pgs 591-608; Wiley-Interscience Pub.		
		WILLIAM STALLINGS; 7.3 Memory Management, computer Organization and Architecture, Designing for Performance, 4th Edition; 1996; pgs. 240-263; Prentice Hall NJ		
		HAMCHER, VERANESIC & ZAKY; The Main Memory, Computer ORganization, 2nd Edition; 1984; pgs 306-329; McGraw-Hill Book Company.		
		PATTERSON & HENNESSY; Memory-Hierachy Design, Computer Architecture: A Quantitative Approach; 1990 pgs 408-475; Morgan Kaufmann Pub.; San Mateo, CA.		
		RICHARD KAIN; Advanced Computer Architecture: A Systems Design Approach; 1996; pgs 75-88 and 456-474; Prentice Hall, Englewood Cliffs, NJ.		
		JEAN-LOUP BAER; computer Systems Architecture; 1980 pgs 139-166 and 315-325; Computer Science Press; Rockville, MD.		

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